

<b>Notice of References Cited</b>	Application/Control No. 09/994,574	Applicant(s)/Patent Under Reexamination WHEELER ET AL.	
	Examiner Russ Guill	Art Unit 2123	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A US-7,051,309	05-2006	Crosetto, Dario B.	716/10
B	US-			
C	US-			
D	US-			
E	US-			
F	US-			
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

**FOREIGN PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
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P					
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T					

**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	IEEE Standard Hardware Description Language Based on the Verilog® Hardware Description Language, 1995, IEEE, pages 220 - 221 and 224 - 225
V	
W	
X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.